

## **Abstract**

A method of boosting the voltage supplied to an output pad driver through a bus connected to a voltage regulator. The method comprises momentarily connecting the bus directly to a voltage source and temporarily enabling the voltage regulator to source additional current to an output terminal thereof. A method of boosting the gate voltages for transistors controlling the voltage appearing on output pads of a solid state memory device, the gate voltages supplied by a voltage regulator through an output bus. The method comprises periodically determining the demand for gate voltage and, when the demand is high, momentarily connecting each line of the bus to a voltage source, and temporarily enabling the voltage regulator to source additional current to an output terminal thereof.